

## ***B. Tech Degree V Semester Examination, November 2009***

### **EE 505 MICROPROCESSOR BASED SYSTEMS**

*(2006 Scheme)*

Time : 3 Hours

Maximum Marks : 100

#### **PART A**

*(Answer all questions)*

**(8 x 5 =40)**

- I.
  - a. Explain the interrupt structure of 8085.
  - b. What are the uses of timing diagrams? Draw the timing diagram of the instruction Mov A,B.
  - c. What is DMA? Explain.
  - d. Explain the uses of programmable interrupt controller.
  - e. What do you mean by memory paging? Explain.
  - f. Explain about the protected mode of 80386.
  - g. Explain the various call instruction of 8051.
  - h. Explain the various logical operations of 8051.

#### **PART B**

**(4 x 15 =60)**

- II. Explain the various addressing modes of 8085 with examples.  
**OR**
- III. Explain the architecture of 8086 with a neat diagram.
- IV. Explain with a neat block diagram, the operation of programmable peripheral interface (8255)  
**OR**
- V. Explain the operation of keyboard display interface (8279) with a neat diagram.
- VI. With a neat diagram, explain the architecture of 80386.  
**OR**
- VII. Explain the features of Pentium processor. Also discuss about the branch prediction logic and super scalar architecture.
- VIII. Explain the architecture of 8051 micro controller with a neat block diagram.  
**OR**
- IX. Explain the instruction set of 8051 with examples.

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