Reg. No.

# B. Tech. Degree IV Semester Examination April 2019

## CS/IT 406 DATA COMMUNICATION

(2006 Scheme)

Time : 3 Hours

I.

Maximum Marks: 100

#### PART A

#### (Answer ALL questions)

 $(8 \times 5 = 40)$ 

 $(4 \times 15 = 60)$ 

- (a) List out the advantages of digital transmission over analog transmission.
  - (b) Define channel capacity. A data is to be transmitted over PSTN using 8 levels per signal elements. If the bandwidth is 3000HZ, deduce maximum data transfer rate.
  - (c) Illustrate Biphase and Bipolar schemes.

(d) Explain the need of scrambling techniques with suitable example.

- (e) Explain the frame format in HDLC.
- (f) Explain the concept of redundancy in error detection schemes.
- (g) Write a short note on CDMA.
- (h) What is DSL? List any three variants of DSL.

### PART B

П	(a)	Summarize the various transmission impairments	(10)
	$(\mathbf{a})$	Explain wireless transmission	(10)
	(0)	Explain whereas italishiission.	(3)
-		OR	
III.	(a)	List out the advantages of optical communications.	(5)
	(b)	Compare the characteristics of coaxial cables and twisted pair cables.	(10)
IV.		Illustrate PCM and delta modulation.	(15)
		OR	
V.	(2)	Construct a Huffman code tree with probabilities $0.4 \pm 0.19$	(9)
	(4)	0.16 0.15 0.1 Find put the quere se code size and variance	$(\mathcal{I})$
		0.16, 0.15, 0.1. Find out the average code size and variance.	
	(b)	Explain LZW coding.	(6)
VI.	(a)	Explain how error detecting is done using CRC. Implement CRC for the	(9)
	. ,	message 11100110 for the generator polynomial 11001.	
	$(\mathbf{h})$	Explain the significance of Hamming Code in forward error correction	(6)
	(0)	OD	(0)
<b>1</b> / <b>1 1</b>			(1.5)
VII.		Summarize three categories of ARQ error control mechanisms.	(15)
/III.		With the help of necessary figures explain DSSS and FHSS.	(15)
		OR	
IY		Illustrate Synchronous and Asynchronous Time Division Multipleving	(15)
$1^{\Lambda}$ .		musuale Synchronous and Asynchronous Time Division Multiplexing.	. (15)

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